M. Tech. 2nd Semester (Computer Engg.)
Examination, June–2009

SOFTWARE VERIFICATION VALIDATION AND TESTING

Paper–MTCE-608-A

Time allowed : 3 hours]   *[Maximum marks : 100*

Note : *Attempt any five questions.*

1.   (a)   What is the difference between errors, bug, incided and test oracle ?   10

     (b)   Explain five testing principles.   10

2.   Give methods for verifying HLD and LLD.   20

3.   (a)   What is the role of checklist in verification ?   10

     (b)   Discuss the validation test plans and validation testing in V & V diagram.   10

4.   How do you prepare classes in equivalence class partitioning method ? Give example of designing test cases with this method.   20

5.   Write a program for finding odd and even numbers. Draw DD graph for this program and design test cases using basis path testing.   20

6. (a) What are advantages of static testing over dynamic testing ? 10

   (b) Explain notations for cause-effect graphing technique. 10

7. How do you perform recovery and usability testing ? 20

8. (a) Is debugging a type of testing ? Explain. 10

   (b) Explain risk prioritization scheme for reducing test cases. 10